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	[Abstract] [PDF Full-Text (244 KB)] IEEE CNF 2 A comparison of full and partial predicated execution support for ILP processors Mahlke, S.A.; Hank, R.E.; McCormick, J.E.; August, D.I.; Hwu, W.W.; Computer Architecture, 1995. Proceedings. 22nd Annual International Symposium on , 22-24 June 1995 Pages:138 - 149 [Abstract] [PDF Full-Text (1220 KB)] IEEE CNF

3 Characterizing The Impact Of Predicated Execution On

Branch Prediction

Mahlke, S.A.; Hank, R.E.; Bringmann, R.A.; Gyllenhaal, J.C.;

Gallagher, D.M.; Weri-mei W. Hwu;
Microarchitecture, 1994. MICRO-27. Proceedings of the 27th
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Pages:217 - 227

[Abstract] [PDF Full-Text (1068 KB)] IEEE CNF

4 Effective Compiler Support For Predicated Execution Using The Hyperblock

Mahlke, S.A.; Lin, D.C.; Chen, W.Y.; Hank, R.E.; Bringmann, R.A.; Microarchitecture, 1992. MICRO 25., Proceedings of the 25th Annual International Symposium on , 1-4 Dec. 1992 Pages:45 - 54

[Abstract] [PDF Full-Text (904 KB)] IEEE CNF

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	L42	vliw and predicat\$4	174	
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	L40	predicat\$3 and L38	7	
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	L36	"both predicated instruction paths"	0	
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	L34	"both predicated paths"	0	
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	L32	predicat\$3 and L31	0	
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	L29	"both branch paths"	26	
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	L27	predicate with multiple conditions	0	
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	L26	predicat\$3 and L25	7	
	L25	microinstruction\$1 or micro-instruction\$1 or micro-code or micro-operation\$1 or micro-operation\$1 or micro-op\$1 or micro-op\$1 or micro-program\$1 or microprogram\$1	11192	
	DB=U	JSPT,PGPB; PLUR=NO; OP=ADJ		
	L24	L23 with predicat\$3	5	
	L23	microinstruction\$1 or micro-instruction\$1 or micro-code or micro-operation\$1 or micro-op\$1 or micro-op\$1 or micro-program\$1 or microprogram\$1	13431	
	DB = U	JSPT; PLUR=NO; OP=ADJ		
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	L19	memory controller\$1 with input/output controller\$1	64
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	L17	predicat\$2 with commit\$3	34
	L16	predicat\$2 and commit\$3	873
	L15	commit\$3 and L11	0
	L14	squash\$3 and L11	0
	L13	Quash\$3 and L11	0
	L12	write back and L11	0
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	L9	(6052776 6427234 6446258)![pn]	3
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